

FIG. 1A

C/A CODE

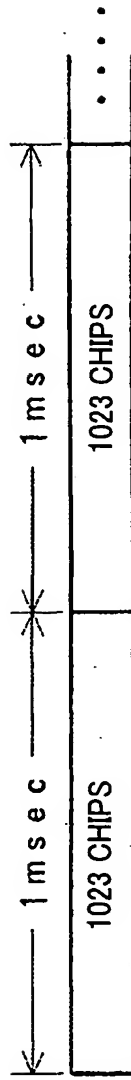


FIG. 1B

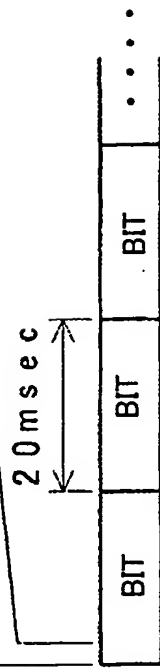


FIG. 1C

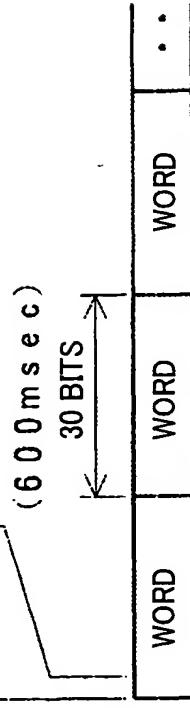


FIG. 1D



FIG. 1E

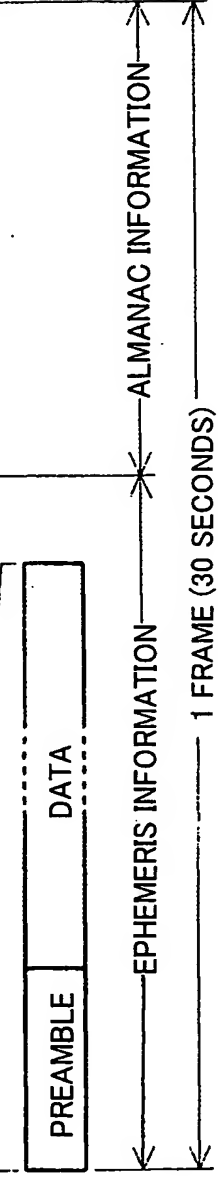


FIG. 2

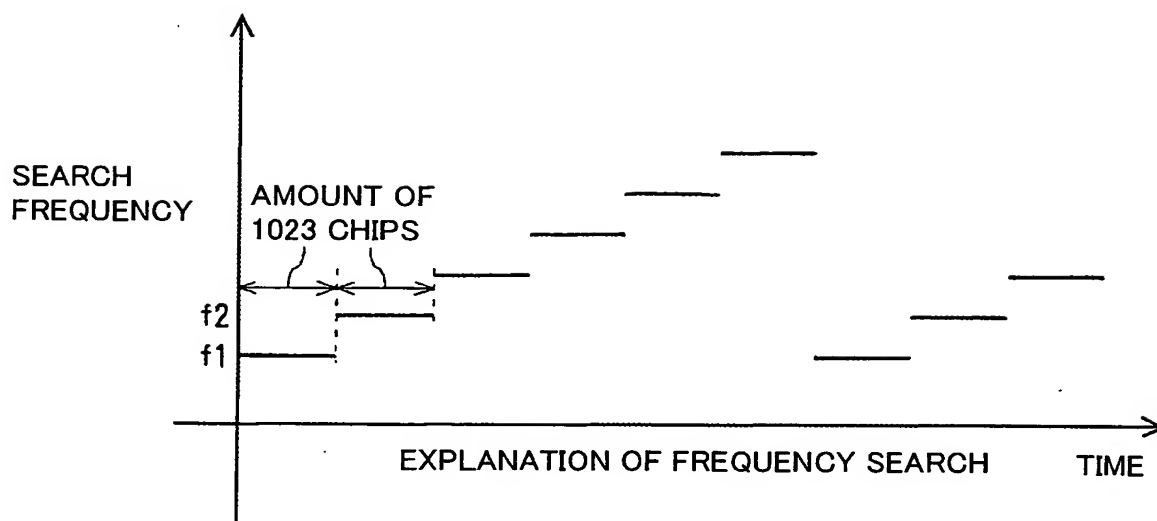


FIG. 3

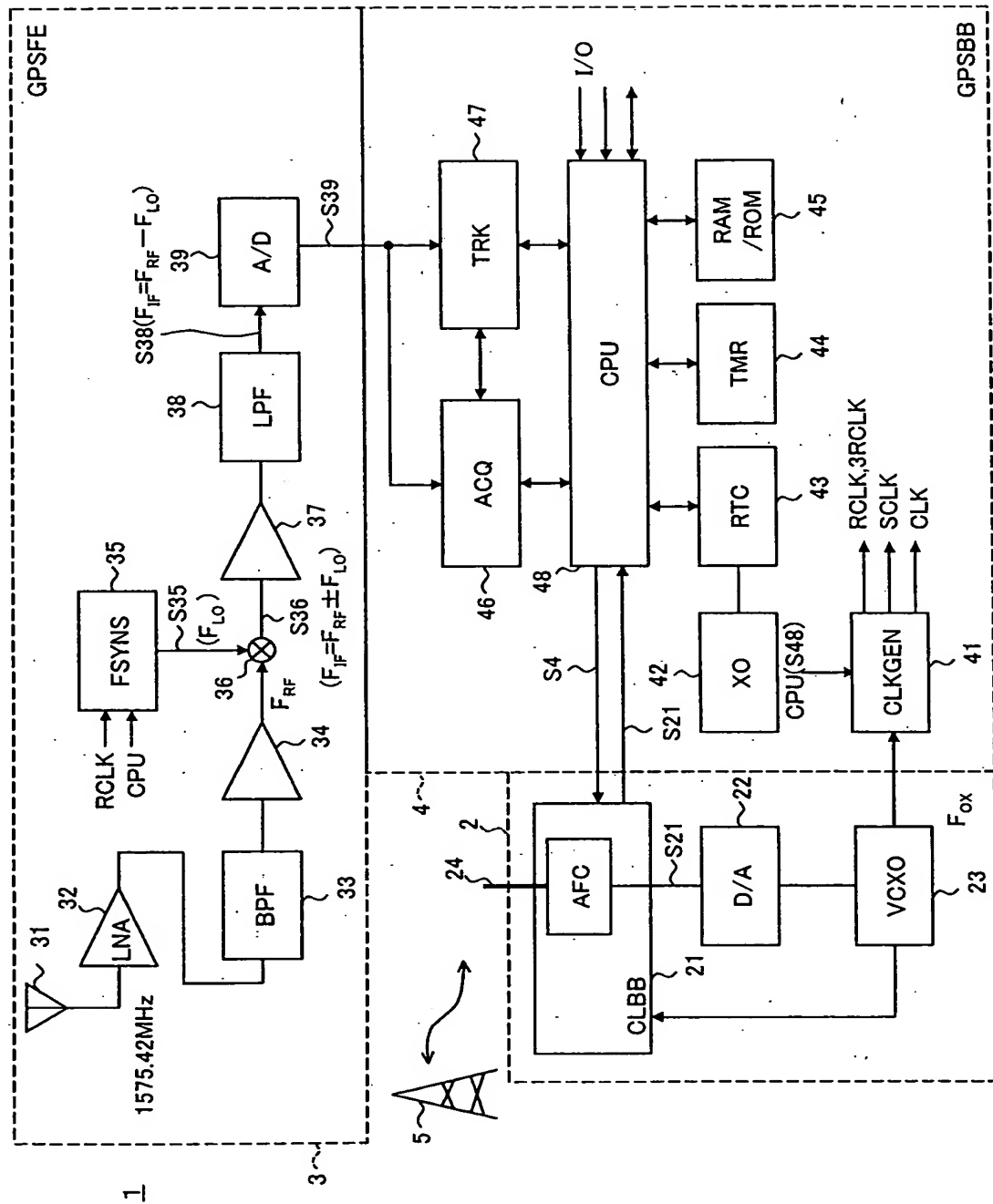


FIG. 4

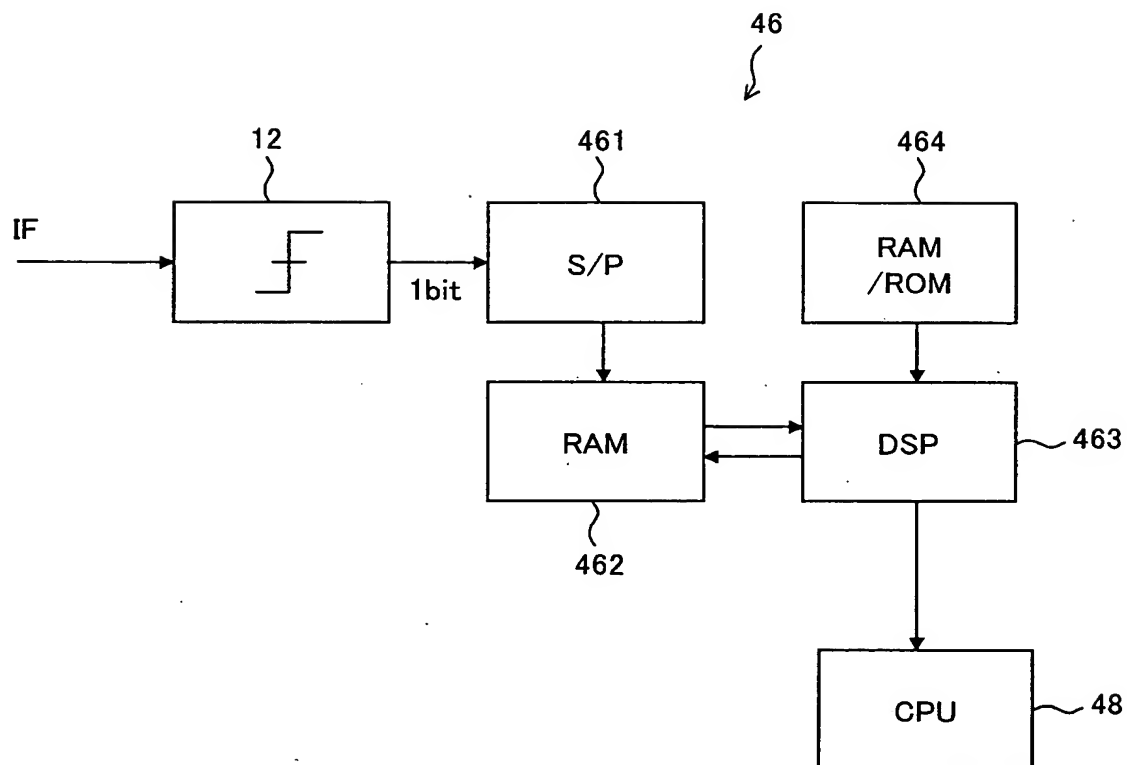


FIG. 5

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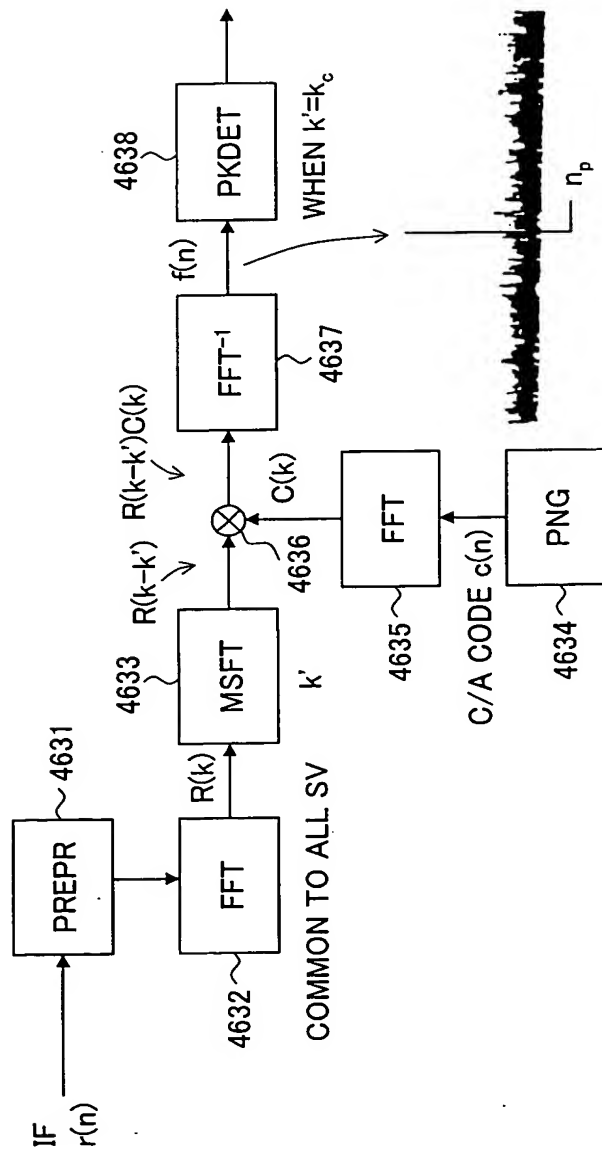


FIG. 6

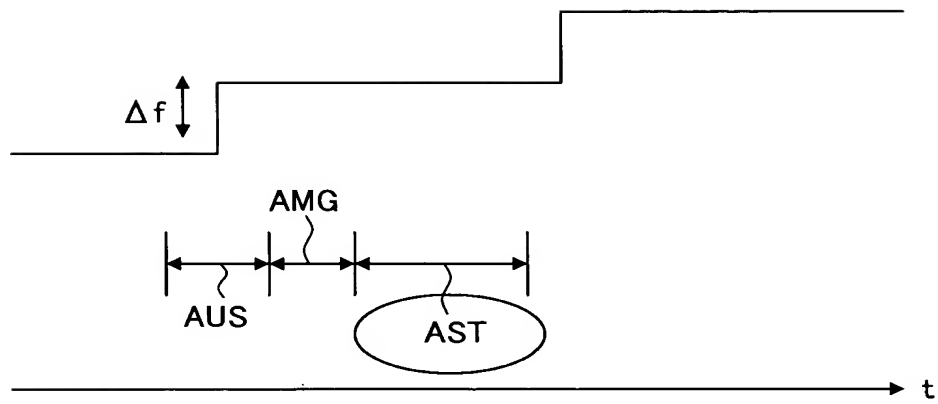


FIG. 7

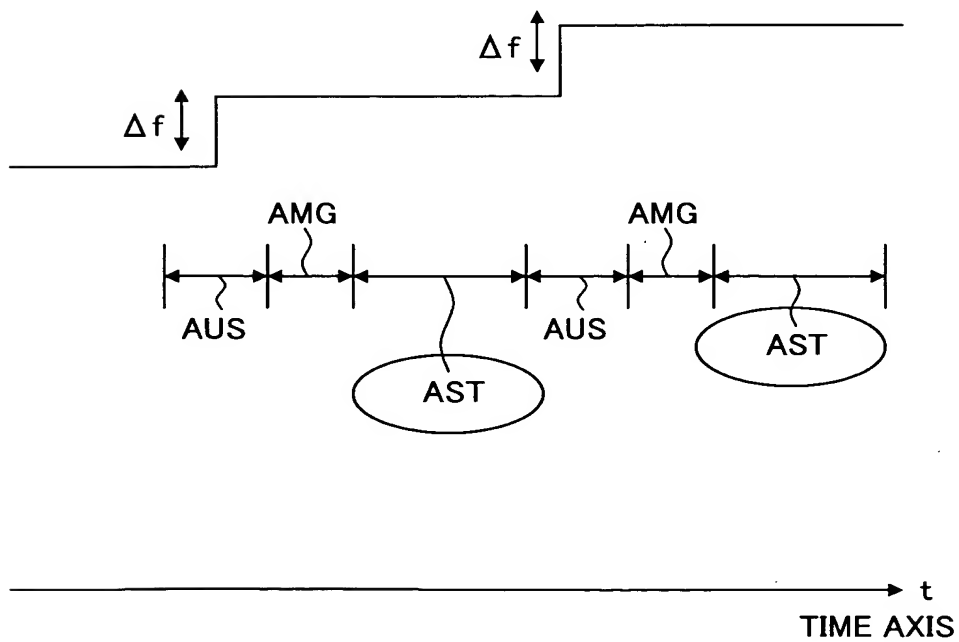


FIG. 8

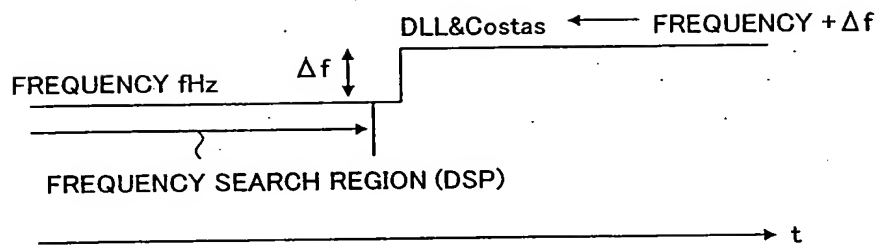


FIG. 9A

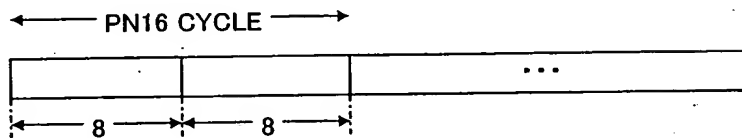


FIG. 9B

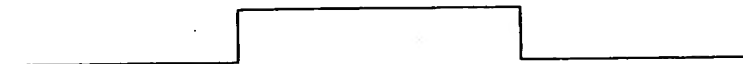


FIG. 9C



FIG. 9D

NAVIGATION MESSAGE



BIT TRANSITION

FIG. 10

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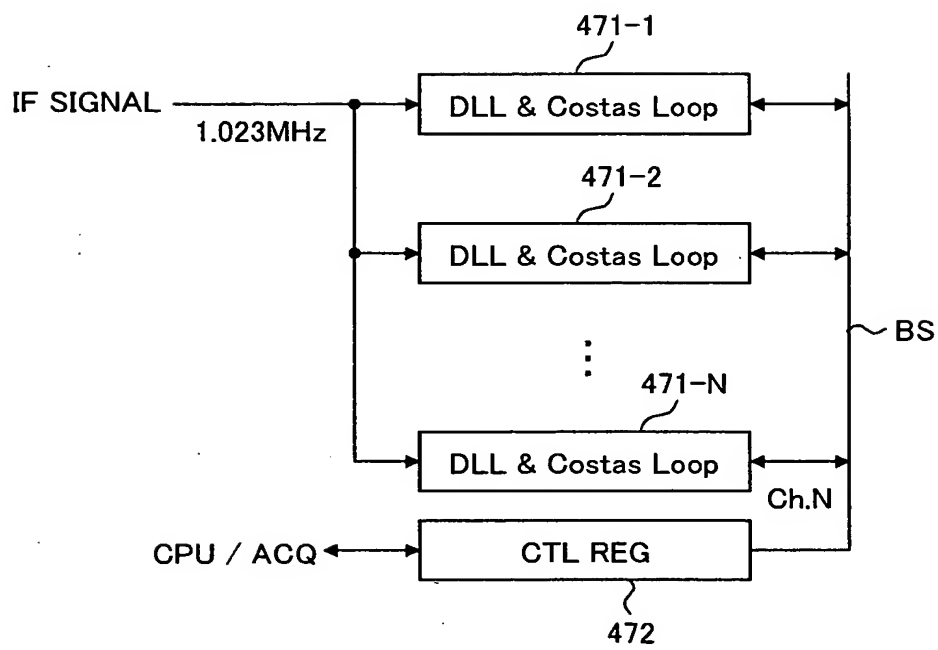




FIG. 11

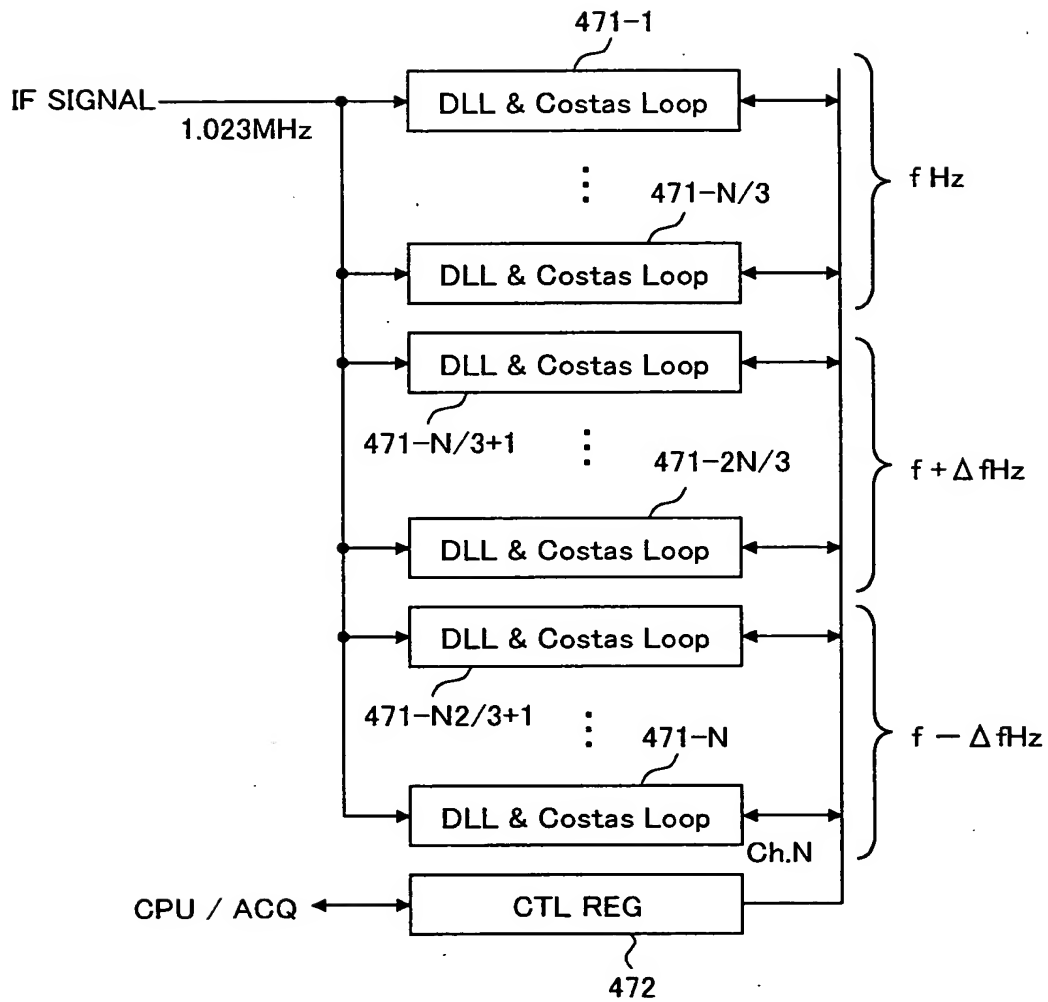
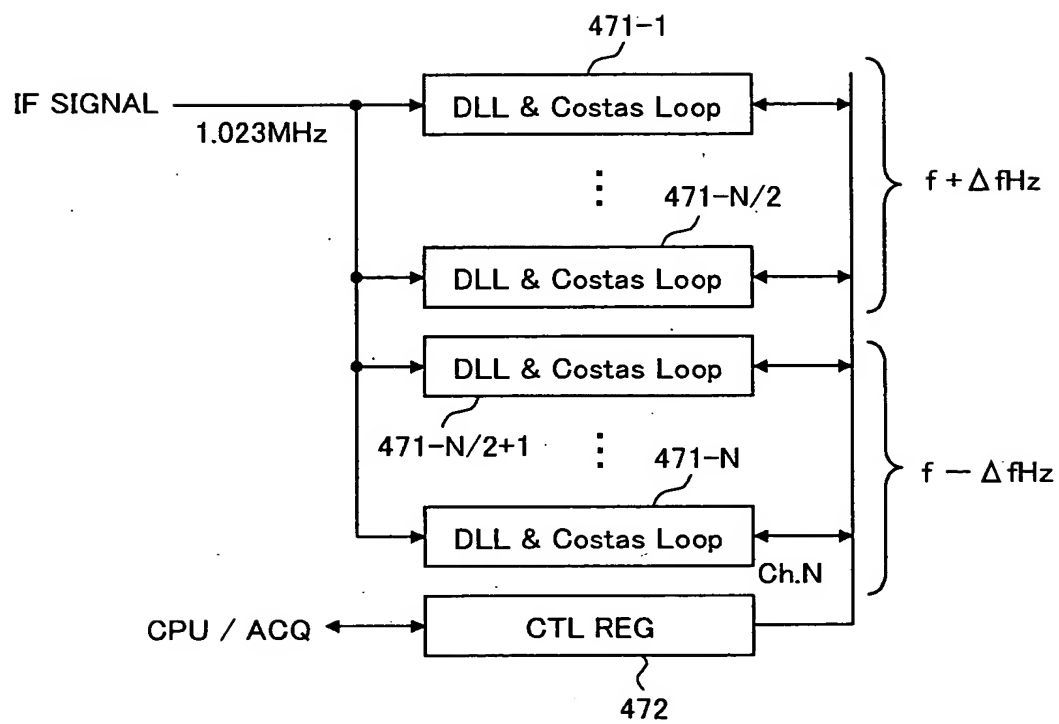


FIG. 12



# FIG. 13

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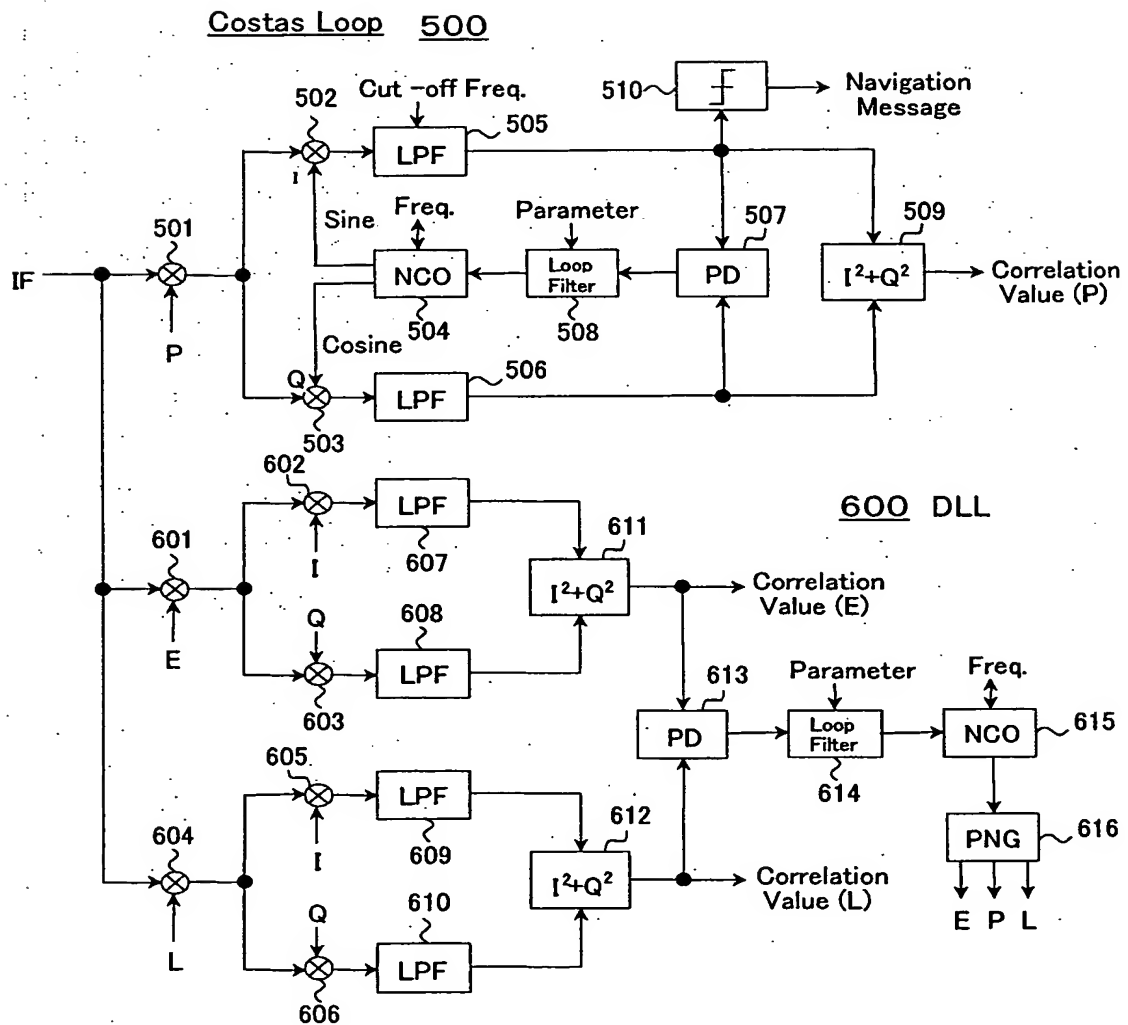


FIG. 14A

FIG. 14B

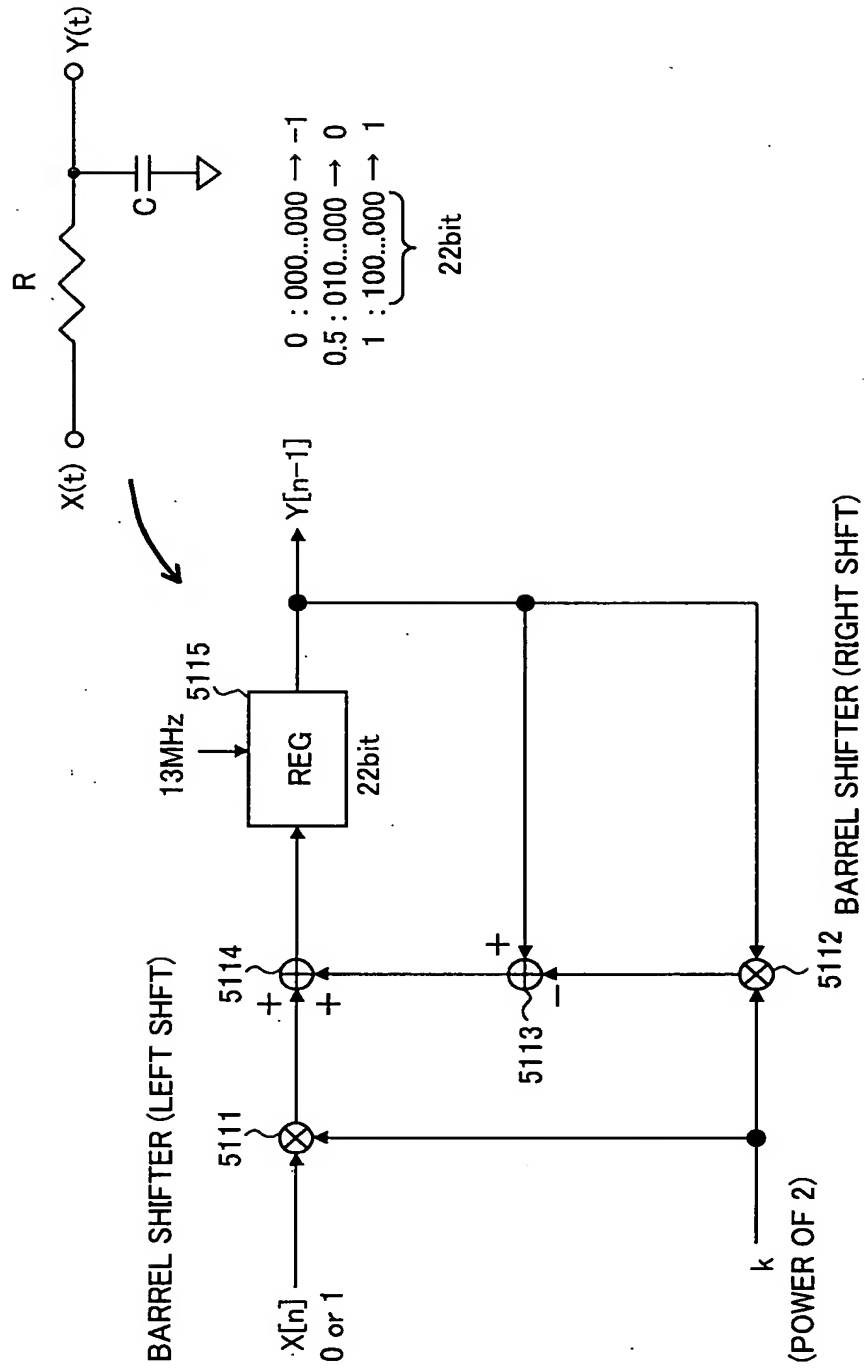


FIG. 15

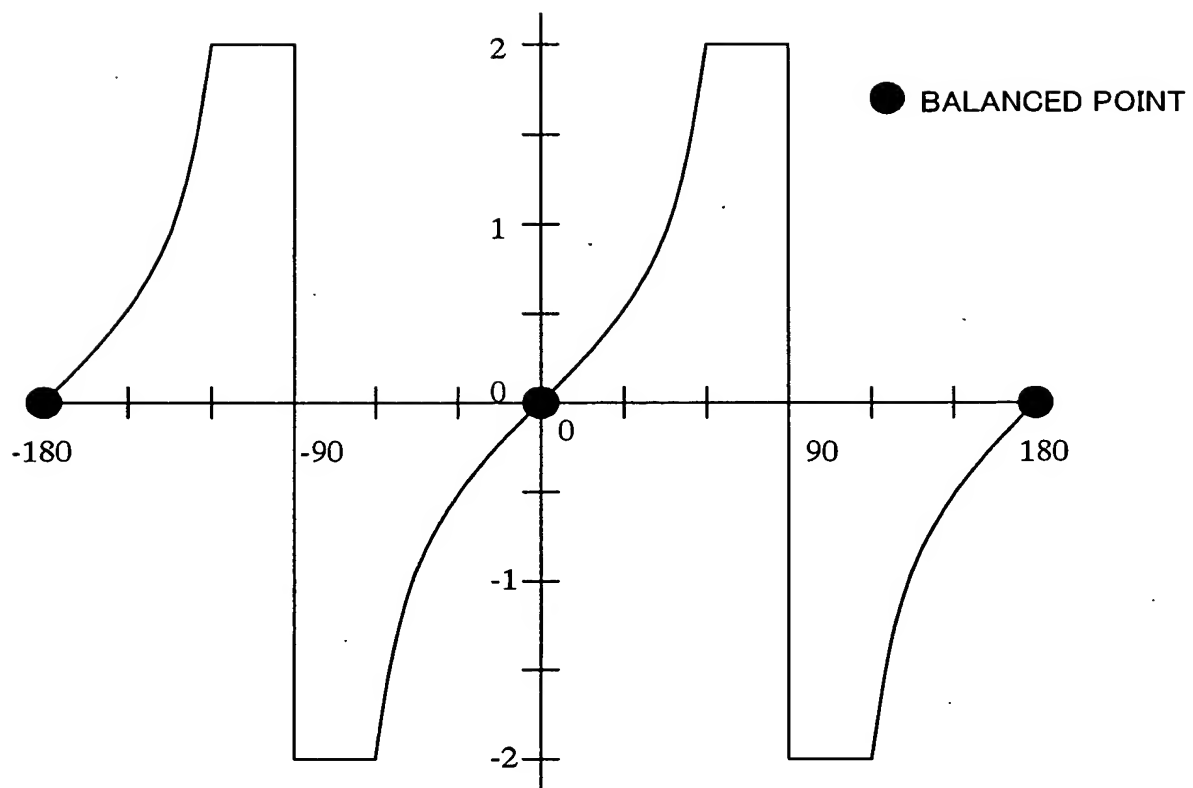


FIG. 16A

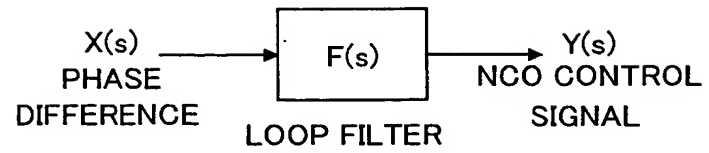


FIG. 16B

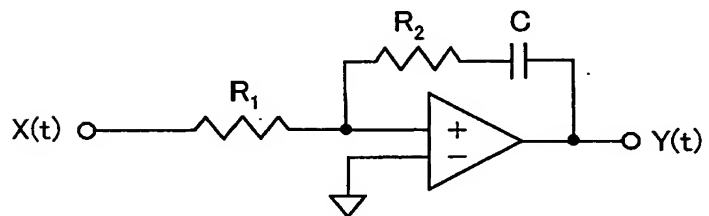
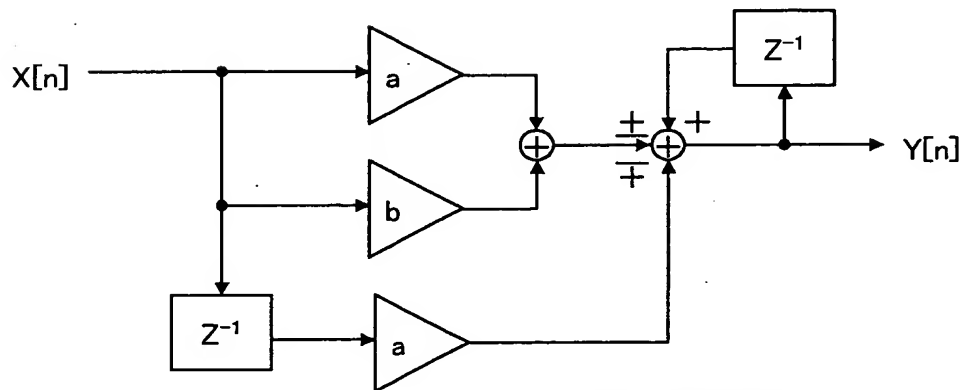


FIG. 16C



$a, b$  ARE POWER OF 2  
(MULTIPLICATION IS SHIFT OPERATION)

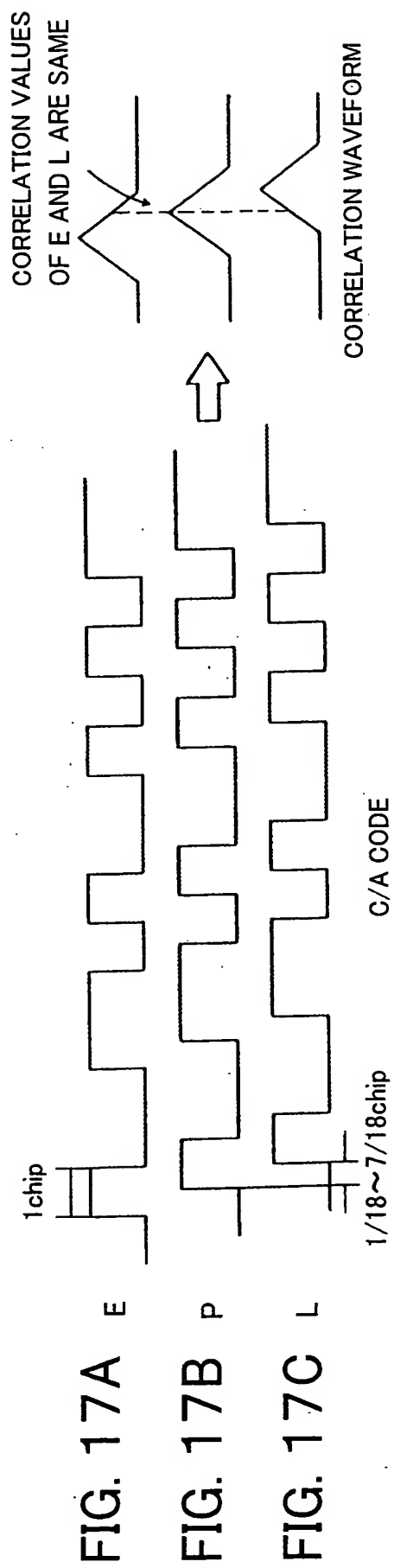


FIG. 18A

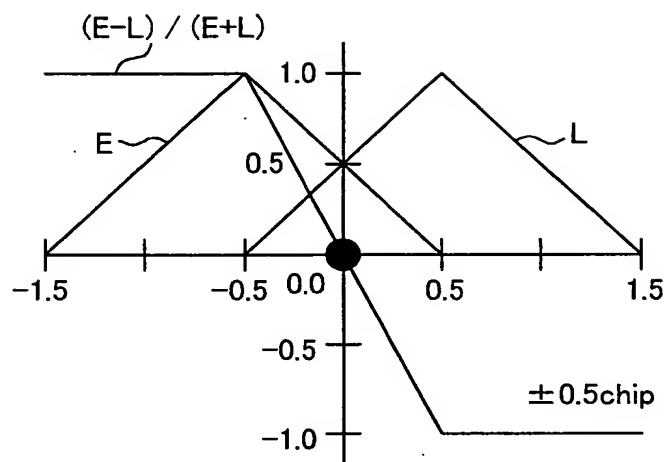


FIG. 18B

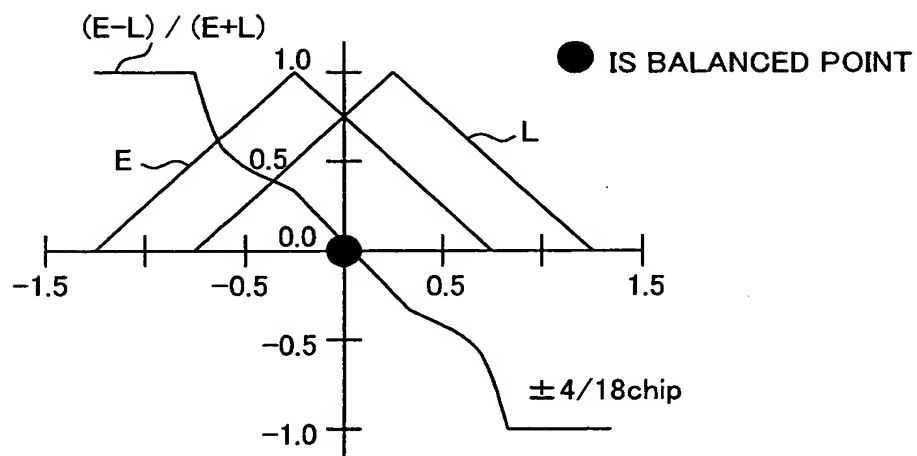




FIG. 19

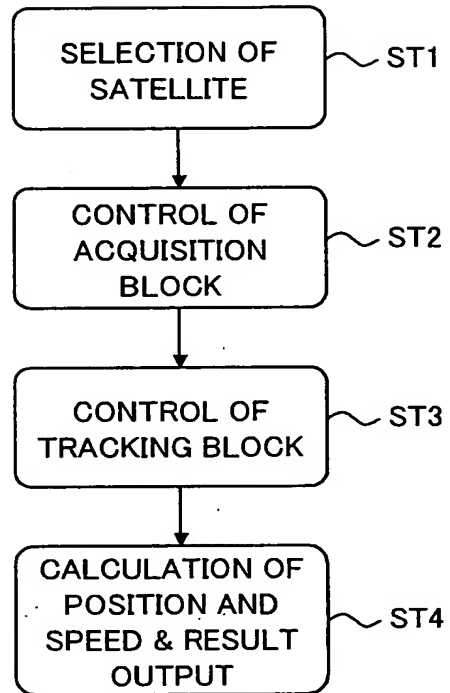


FIG. 20

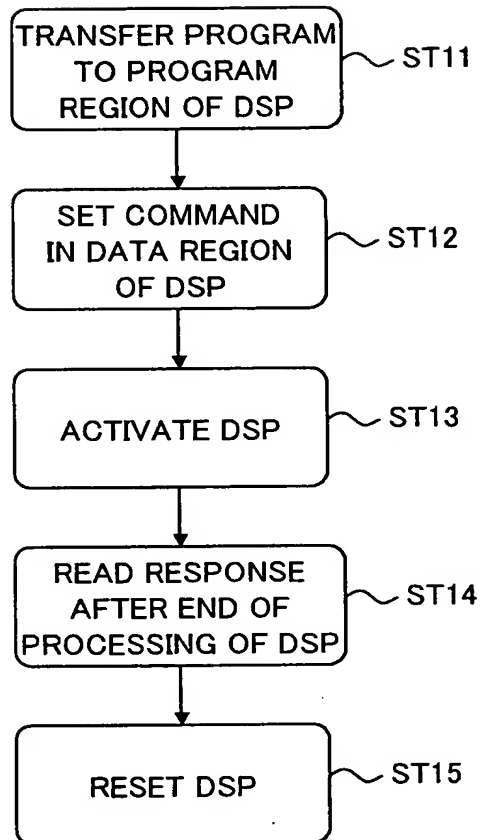


FIG. 21

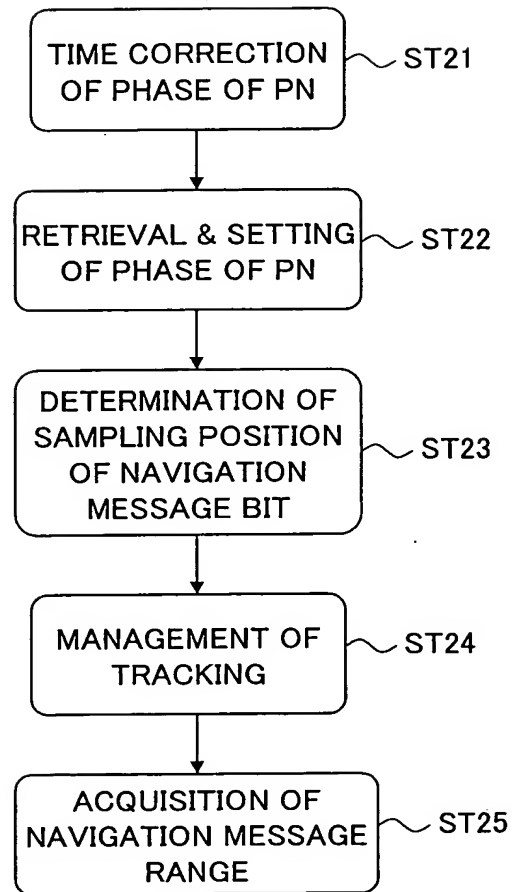


FIG. 22A

CASE WHERE IT CAN BE  
CORRECTLY ESTIMATED

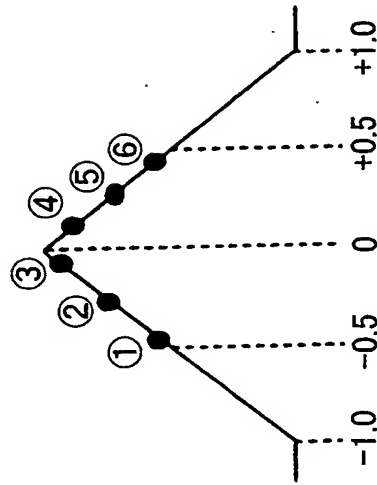


FIG. 22B

CASE WHERE IT IS  
DEVIATED BY -0.5 CHIP

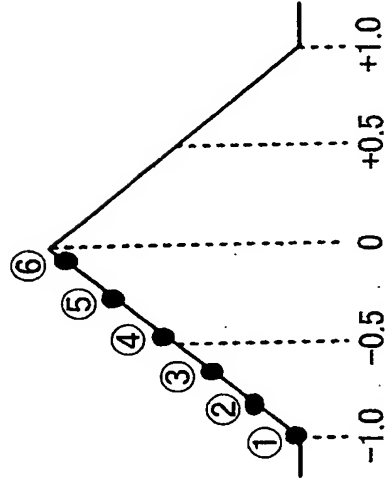


FIG. 22C

CASE WHERE IT IS  
DEVIATED BY +0.5 CHIP

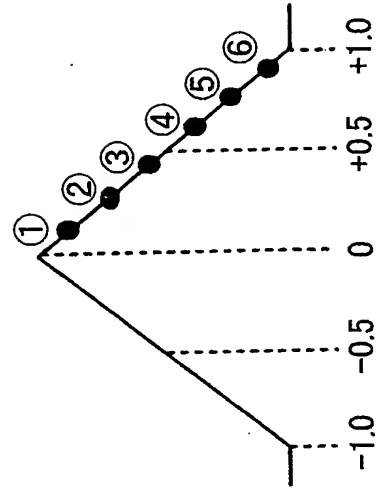


FIG. 23A

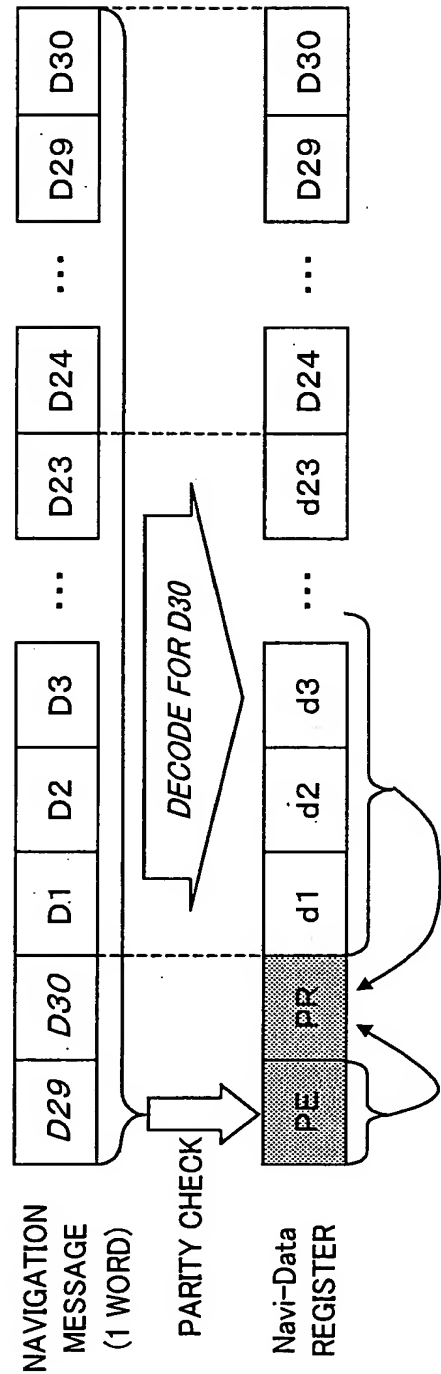


FIG. 23B

